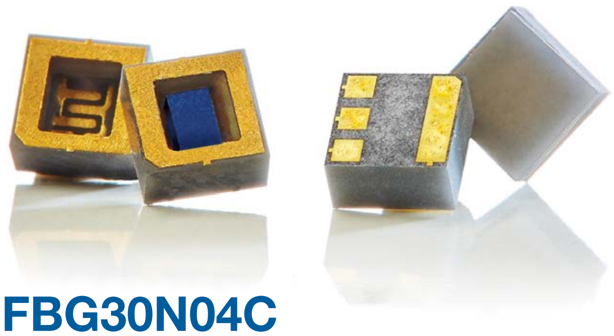
FBG30N04C Datasheet

Features

- Low $R_{DS(on)}$
- Ultra-low Q_G For High Efficiency
- Logic Level
- Light Weight – 0.113 grams
- New Compact Hermetic Package
- Source Sense Pin
- Total Dose
  - Rated to 300 krad
- Single Event
  - SEE immunity for LET of 83.7 MeV/mg/cm^2 with V_{DS} up to 100% of rated Breakdown
- Low Dose Rate at 100 mRad/sec
  - Maintains Pre-Rad specification
- Neutron
  - Maintains Pre-Rad specification for up to 1 x 10^{13} Neutrons/cm^2

Application

- Commercial Satellite EPS & Avionics
- Deep Space Probes
- High Speed Rad Hard DC-DC Conversion
- Rad Hard Motor Controllers



FBG30N04C

Rad Hard eGaN® HEMT 300 V, 4 A, 400 mΩ Surface Mount (FSMD-C)

Description

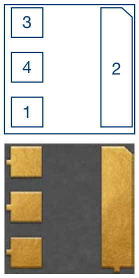
EPC Space FSMD-C series of eGaN® power switching HEMTs have been specifically designed for critical applications in the high reliability or commercial satellite space environments. These devices have exceptionally high electron mobility and a low temperature coefficient resulting in very low $R_{DS(on)}$ values. The lateral structure of the die provides for very low gate charge (Q_G) and extremely fast switching times. These features enable faster power supply switching frequencies resulting in higher power densities, higher efficiencies and more compact packaging.

Thermal Characteristics

Symbol	Parameter-Conditions	Value	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient (Note 3)	64	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	16.35	

I/O Pin Assignment *(Bottom View)*

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	SS	Source Sense
4	S	Source



Absolute Maximum Rating *(T_C = 25°C unless otherwise noted)*

Symbol	Parameter-Conditions	Value	Units
V_{DS}	Drain to Source Voltage (Note 1)	300	V
I_D	Continuous Drain Current ID @ V_{GS} = 5 V, T_C = 25°C, $R_{\theta JA}$ < 62 °C/W	4	A
I_{DM}	Single-Pulse Drain Current t_{pulse} ≤ 80 µs	12	
V_{GS}	Gate to Source Voltage (Note 2)	+6 / -4	V
T_J , T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C
T_{sol}	Package Mounting Surface Temperature	260	°C
ESD	ESD Class	ΔA	

Electrical Characteristics *(T_C = 25°C unless otherwise noted. Typical (TYP) values are for reference only.)*

Parameter	Symbol	Test Conditions		MIN	TYP	MAX	Units
Minimum Drain to Source Voltage	V_{DSMIN}	V_G = 0 V		300			V
Drain to Source Leakage	I_{DSS}	V_{DS} = 300 V, V_{GS} = 0 V	T_C = 25°C	–	10	100	µA
			T_C = 125°C	–	240	500	
Gate to Source Forward Leakage	I_{GSSF}	V_{GS} = 5 V	T_C = 25°C	–	250	500	
Gate to Source Reverse Leakage	I_{GSSR}	V_{GS} = -4 V	T_C = 25°C	-60	-20		
Gate to Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 0.6 mA	T_C = 25°C	0.8	1.2	2.8	V
Gate to Source Threshold Voltage Temperature Coefficient	$\Delta V_{GS(th)}/\Delta T$	$V_{DS} = V_{GS}$, I_D = 0.6 mA	-55°C < T_A < 25°C	–	|0.5|	–	mV/°C
Drain to Source Resistance (Note 4)	$R_{DS(on)}$	I_D = 4 A, V_{GS} = 5 V	T_C = 25°C	–	210	400	mΩ
Source to Drain Forward Voltage (Note 5)	V_{SD}	I_S = 0.5 A, V_G = 0 V	T_C = 25°C		1.75		V

Dynamic Characteristics *(T_C = 25°C unless otherwise noted. Typical (TYP) values are for reference only.)*

Parameter	Symbol	Test Conditions	MIN	TYP	MAX	Units
Input Capacitance	C_{ISS}	f = 1 MHz, V_{DS} = 150 V, V_{GS} =0 V (Note 6)		380	450	pF
Output Capacitance	C_{OSS}			48	60	
Reverse transfer Capacitance	C_{RSS}			2	4	
Gate Resistance	R_G	f = 1 MHz, $V_{DS} = V_{GS}$ = 0 V		0.4		Ω
Total Gate Charge (Note 7)	Q_G	I_D = 4 A, V_{GS} = 5 V, V_{DS} = 150 V		1.6	2.6	nC
Gate to Drain Charge (Note 7)	Q_{GD}	I_D = 4 A, V_{GS} = 5 V, V_{DS} = 150 V		1.1	1.3	
Gate to Source Charge (Note 7)	Q_{GS}	I_D = 4 A, V_{GS} = 5 V, V_{DS} = 150 V		0.9	1.7	
Output Charge (Note 8)	Q_{OSS}	V_{GS} = 0 V, V_{DS} = 150 V		40		
Source to Drain Recovery Charge	Q_{RR}	I_D = 4 A, V_{DS} = 150 V		<1		

Radiation Characteristics

EPC Space eGaN® HEMTs are tested according to MIL-STD-750 Method 1019 for total ionizing dose validation. Every manufacturing lot is tested for total ionizing dose of Gamma radiation with an in-situ bias for (i) V_{GS} = 5 V, (ii) V_{DS} = V_{GS} = 0 V and (iii) V_{DS} = 80% B_{VDSS}.

Electrical Characteristics up to 300 krads *(T_C = 25°C unless otherwise noted. Typical (TYP) values are for reference only.)*

Parameter	Symbol	Test Conditions	MIN	TYP	MAX	Units
Maximum Drain to Source Voltage	V_{DSMAX}	V_{GS} = 0 V	–	–	300	V
Gate to Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS} , I_D = 0.6 mA	0.8	1.2	2.8	
Drain to Source Leakage	I_{DSS}	V_{DS} = 300 V, V_{GS} = 0 V	–	10	290	µA
Gate to Source Forward Leakage	I_{GSS}	V_{GS} = 5 V	–	250	500	
Gate to Source Reverse Leakage	I_{GSS}	V_{GS} = -4 V	–	-20	-100	
Drain to Source Resistance (Note 4)	$R_{DS(on)}$	I_D = 4 A, V_{GS} = 5 V	–	-	400	mΩ

Typical Single Event Effect Safe Operating Area

Note : All Single Event Effect testing is performed on the K-500 Cyclotron at Texas A&M University

Test	Environment				V_{DS} Voltage (V)	
	Ion	LET MeV/mg/cm^2	Range µm	Energy MeV	V_{GS} = 0 V	V_{GS} = -4V
See SOA	Xe	50	131	1653	300	300
	Au	83.7	130	2482	300	300

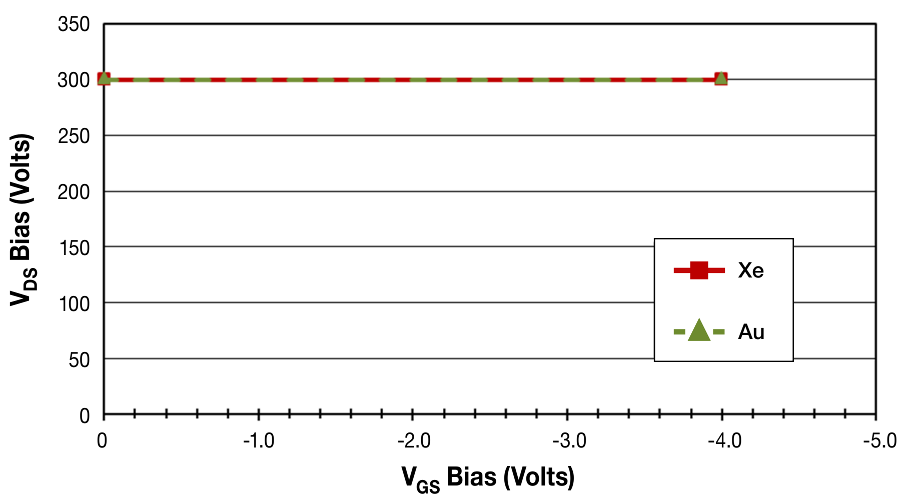


Figure 1. Typical Single Event Effect Safe Operating Area

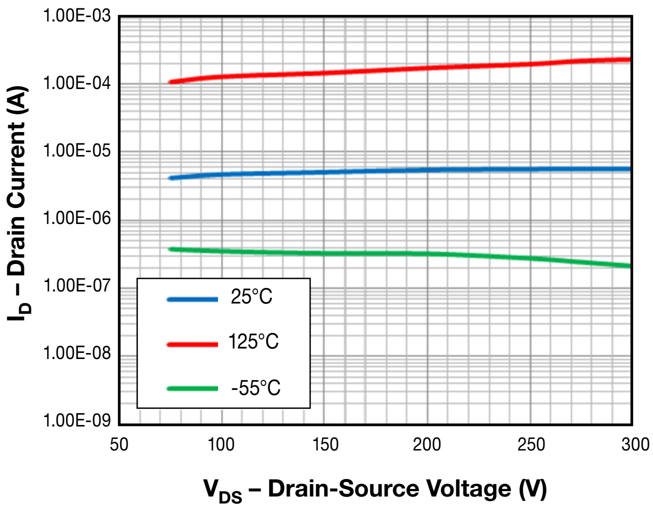


Figure 2. Typical Gate-Source Leakage Current vs. Ambient Temperature

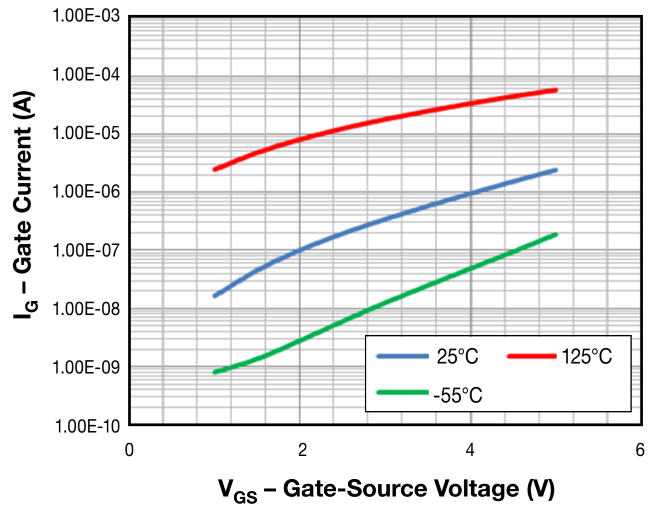


Figure 3. Typical Drain-Source Leakage Current vs. Ambient Temperature

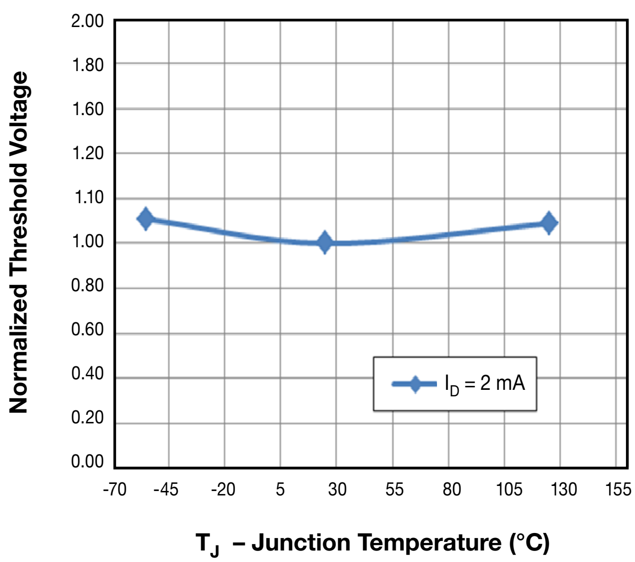


Figure 4. Normalized Threshold Voltage

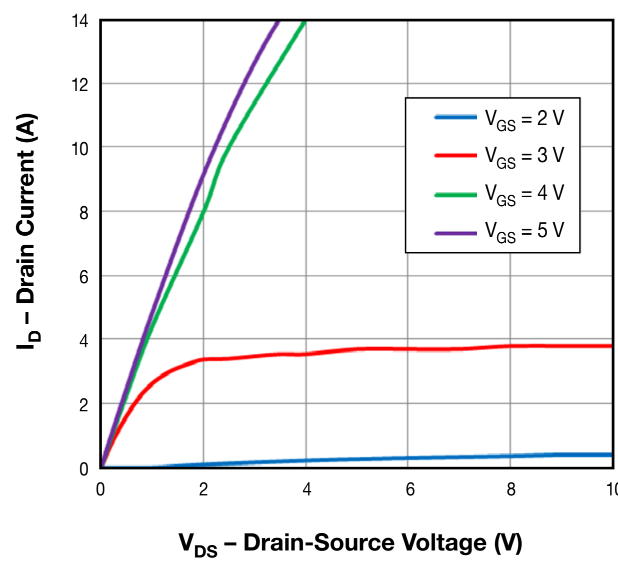


Figure 5. Typical Output Characteristics

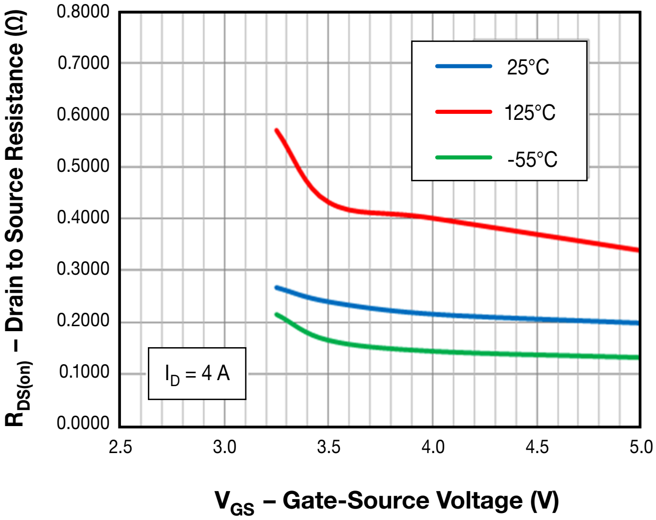


Figure 6. Typical Drain-Source ON Resistance vs. Gate-Source Voltage vs. Ambient Temperature

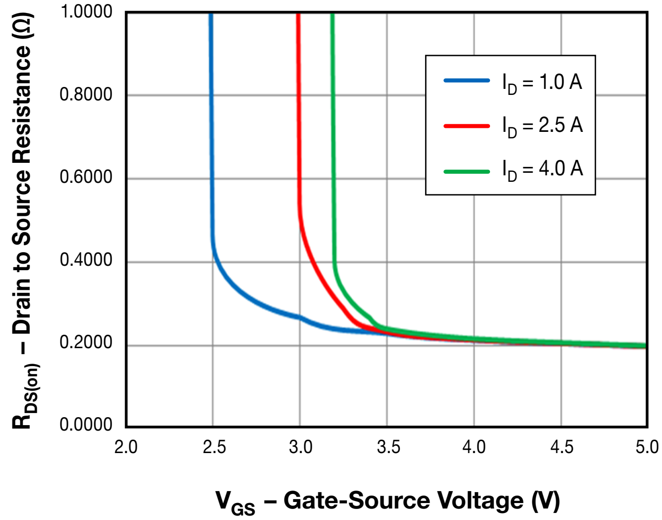


Figure 7. Typical Drain-Source ON Resistance vs. Gate-Source Voltage vs. Drain Current

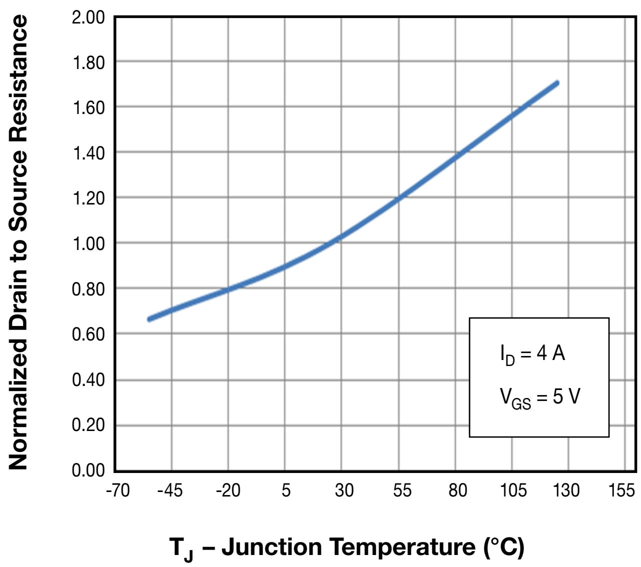


Figure 8. Typical Normalized Drain-Source ON Resistance vs. Ambient Temperature

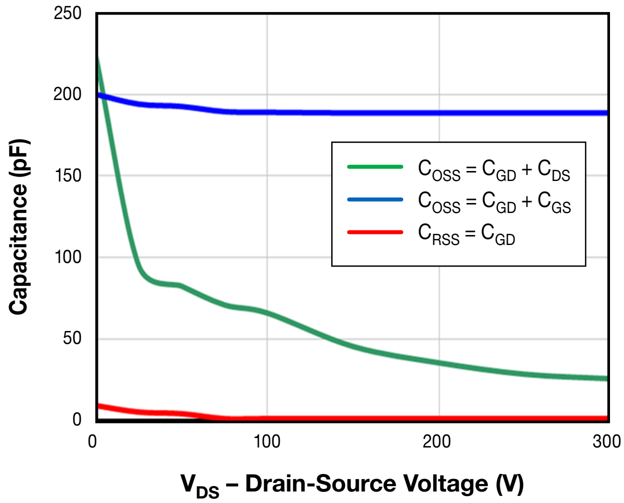


Figure 9. Typical Inter-Electrode Capacitance vs. Drain-Source Voltage

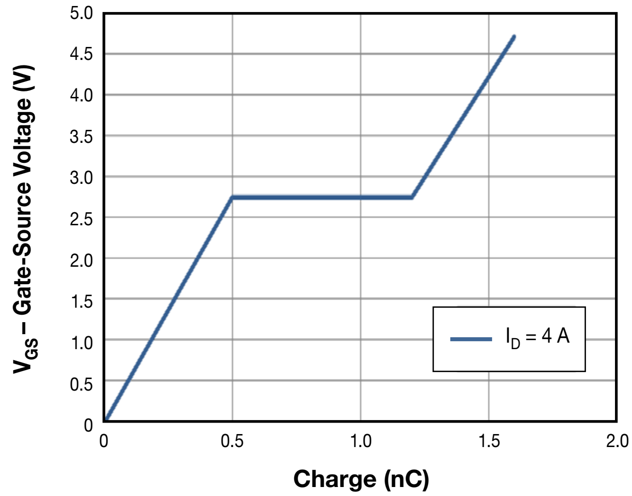


Figure 10. Typical Gate Charge vs. Drain Current

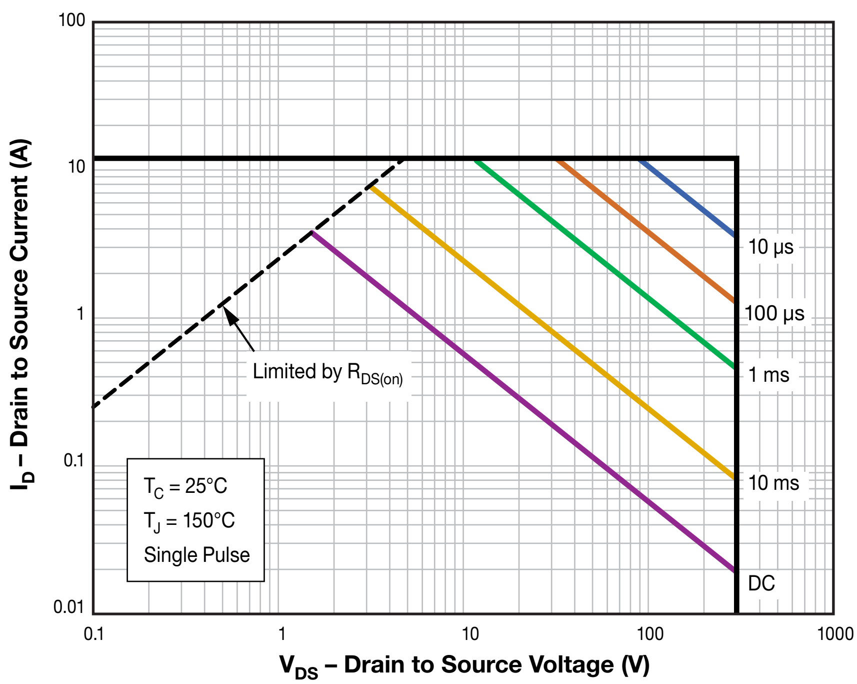


Figure 11. Safe Operating Area

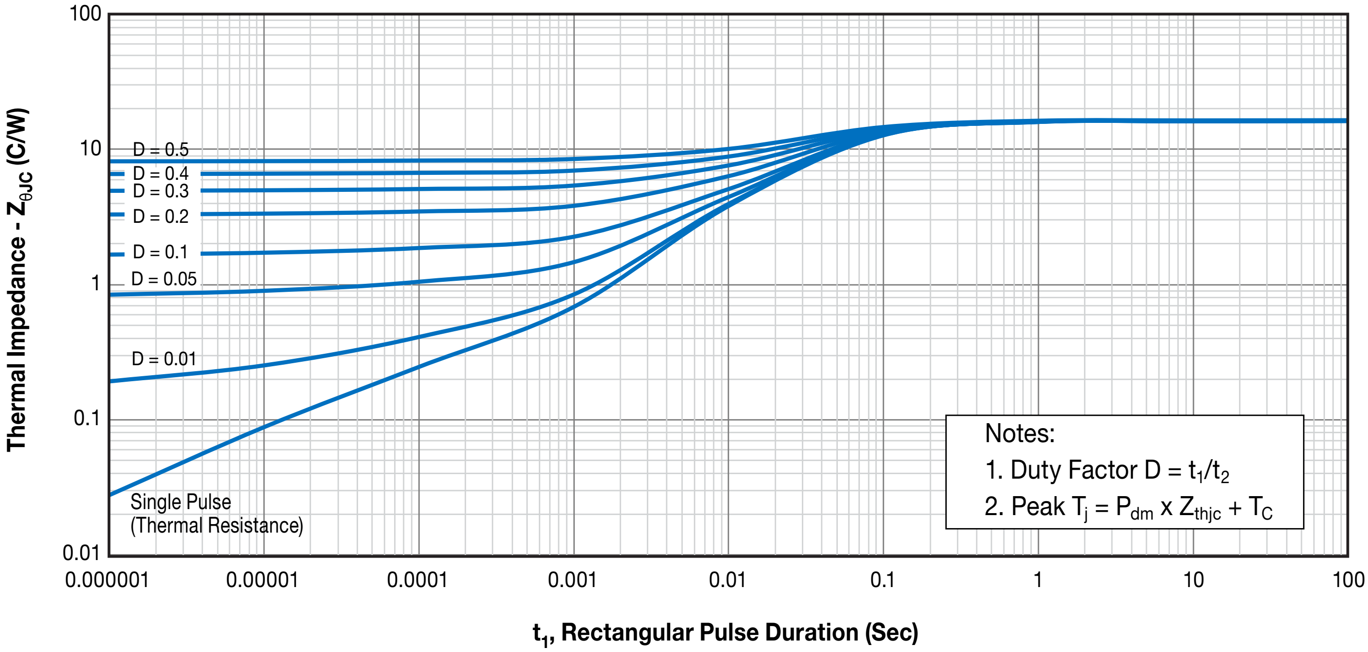


Figure 12. Transient Thermal Impedance, Junction to Case

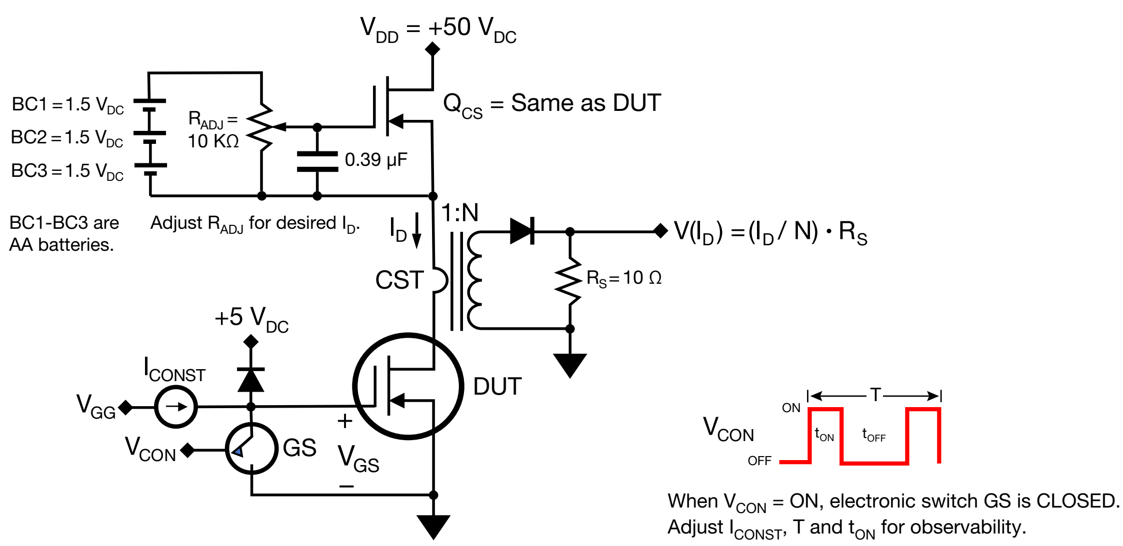


Figure 13. Charge Test Circuit

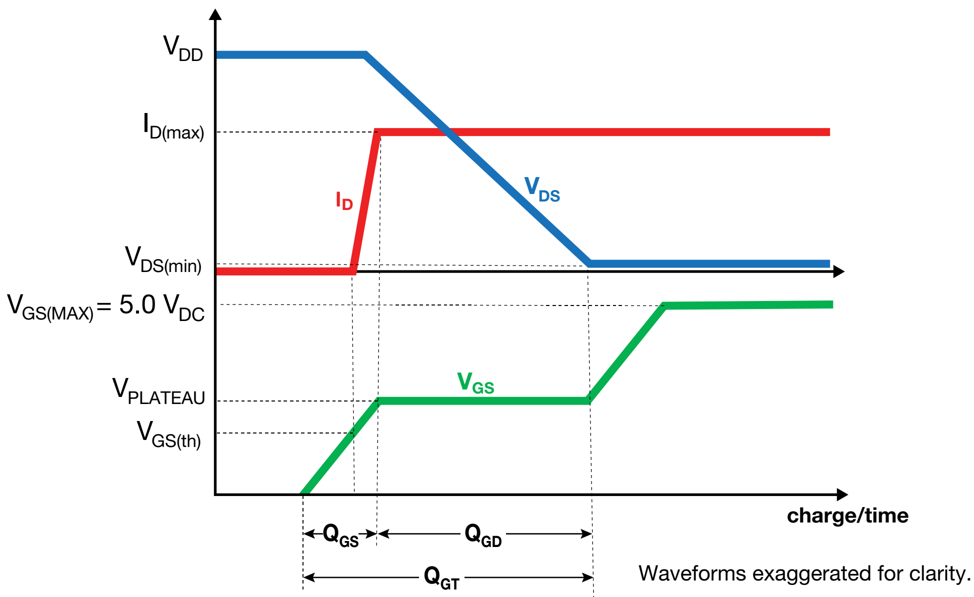
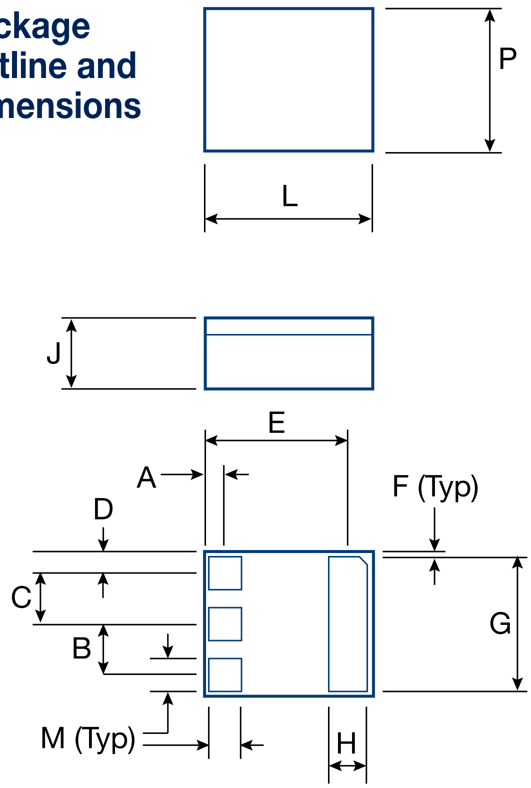


Figure 14. Typical Gate Charge Test Waveform

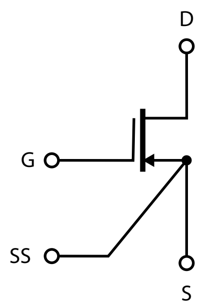
Package Outline and Dimensions



Symbol	Inches		Millimeters		Note
	MIN	MAX	MIN	MAX	
A	0.020	0.030	0.508	0.762	
B	0.055	0.065	1.473	1.727	
C	0.055	0.065	1.321	1.574	
D	0.020	0.030	0.508	0.762	
E	0.140	0.150	3.556	3.810	
F	0.005		0.127		Ref. only
G	0.155	0.165	3.937	4.191	
H	0.035	0.045	0.889	1.143	
J	0.080	0.090	2.032	2.286	Ref. only
L	0.165	0.175	4.191	4.445	
M	0.035	0.045	0.889	1.143	
P	0.165	0.175	4.191	4.445	

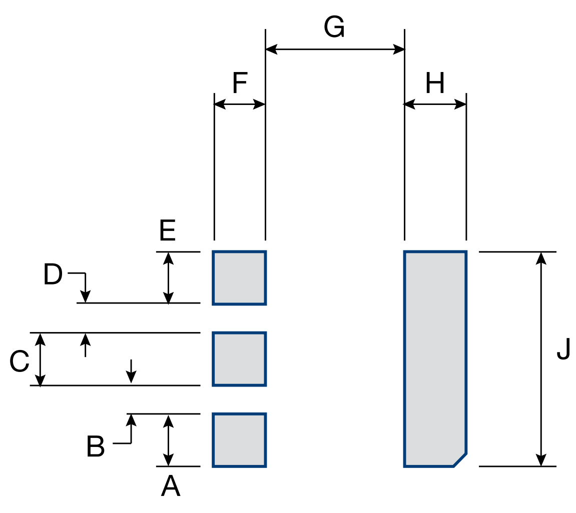
Standard Terminal Pad finish is a solder alloy of 63%Pb 37%Sn

Package Connections



NOTE: SS pin is connected directly to source of internal die.

FSMD-C Footprint for Printed Circuit Board Design



Symbol	Inches		Millimeters		Note
	MIN	MAX	MIN	MAX	
A	0.038	0.048	0.965	1.219	
B	0.012	0.022	0.304	0.558	
C	0.041	0.051	1.041	1.295	
D	0.012	0.022	0.304	0.558	
E	0.038	0.048	0.965	1.219	
F	0.041	0.051	1.041	1.295	
G	0.069	0.079	1.752	2.006	
H	0.041	0.051	1.041	1.295	
J	0.161	0.171	4.089	4.343	

Notes

Note 1. NEVER exceed the absolute maximum V_{DS} of the device otherwise permanent damage/destruction may result.

Note 2. NEVER exceed the absolute maximum V_{GS} of the device otherwise permanent damage/destruction may result. We recommend use at no greater than +5 V as the HEMT is fully conducting at this point.

Note 3. $R_{θJA}$ measured with FSMD-C package mounted to double-sided PCB, 0.063” thickness with 1.0 square inches of copper area on the top (mounting side) and a flood etch (3 square inches) on the bottom side.

Note 4. Measured using four wire (Kelvin) sensing and pulse measurement techniques. Measurement pulse width is 80 µs and duty cycle is 1%, maximum.

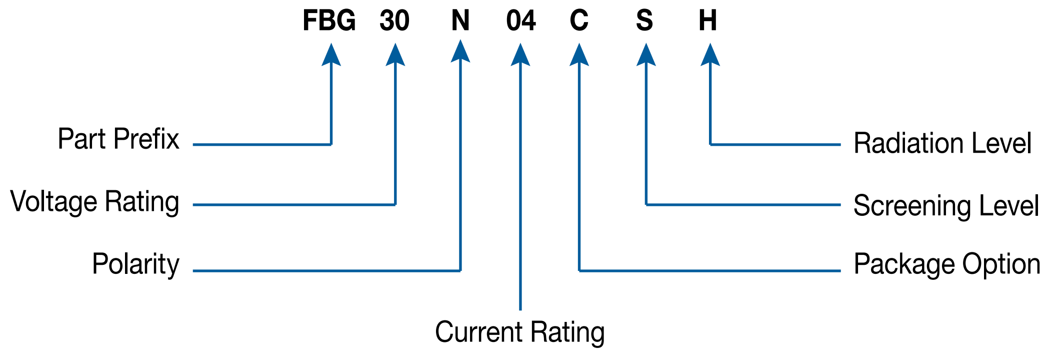
Note 5. With pulse measurement width 100–380 µs.

Note 6. $C_{ISS} = C_{GS} + C_{GD}$ with C_{DS} shorted. $C_{OSS} = C_{DS} + C_{GD}$. $C_{RSS} = C_{GD}$.

Note 7. The gate charge parameters are measured using the circuit shown in Figure 11. Qs and associated components BT1, P1 and C1 form a high speed current source that serves as the test load for the DUT. A constant gate current (I_{const}) of 1.5-3 mA is provided to the Gate of the DUT during the time that the ground switch (G_S) is OFF (t_{off}). The DUT is switched ON and OFF using ground-sensed switch GS. The gate current is adjusted to yield the desired charge per unit time (I_{const} · time per division) on the measuring oscilloscope. The GS pulse drive ON time (t_{on})is adjusted for the desired observability of the gate-source voltage (V_{GS}) waveform. The maximum duty cycle of the ground switch (t_{off} /t_{on}) should be set to 1% maximum. Please note that all gate-related signals are referenced to the “Source Sense” pin on the package. At all times during the measurement, the maximum gate-source voltage is clamped to 5 V_{DC}.

Note 8. Guaranteed by design/device construction. Not tested.

EPC Space Part Number Information



Ordering Information Availability

Screening Options	Rad Assurance Options
1 character	1 character
C = Developmental Unit S = Space Level[1]	H = 1000 krad, LET = 84

Part Number	Screening Level	Shipping
FBG30N04C*C	Developmental Units	Waffle trays
FBG30N04C*S	Space Level	

[1] Screening and qualification consistent to an equivalent MIL-PRF-19500 specification.

FBG30N04CC devices are intended for engineering development purposes only and are NOT intended to be used as flight units.

EPC Space Rad Hard HEMT are not sensitive to Total Ionizing Dose as such the H level covers the R,F,G radiation levels.

Screening Flow Equivalent to a MIL-PRF-19500 General Specification

EPC SPACE Qual Flow Equivalent to a MIL-PRF-19500 Specification					
Operation	**Test**	**Test Methods Per Mil STD 750**	**Sample Size**	**Space Level**	**COT**
Pre-Assembly	Probe Testing	EPC SPACE Internal	100%	✓	✓
	Visual inspection	EPC SPACE Internal	100%	✓	✓
Post-Assembly	Die Shear	2,017	5	✓	✓
	X-Ray	2076	5	✓	✓
Screening	Serilialization		100%	✓	
	Electricals	3411,3413,3421,3404	100%	✓	✓
	Temp Cycling	1051	100%	✓	
	Constant Acceleration	2006	100%	✓	
	PIND	2052	100%	✓	
	Initial Electricals (Read and Record)	3411,3413,3421,3404	100%	✓	
	HTGB	1042 Condition B	100%	✓	
	Interim Electricals (Read and Record)	3411,3413,3421,3404	100%	✓	
	HTRB	1042 Condition A 240 Hours	100%	✓	
	Final Electricals (Read and Record)	3411,3413,3421,3404	100%	✓	
	Final Electricals (High and Low Temperatures)	3411,3413,3421,3404	100%	✓	
	Deltas	Per Procurement Specification	100%	✓	
	Percent Defective Allowable	Per Procurement Specification	100%	✓	
	Dynamic RDSON	EPC SPACE Internal	100%	✓	
	OutLiers Removal	EPC SPACE Internal	100%	✓	
	X-RAY	2076	100%	✓	
	Tinning		100%	✓	
	Hermetic Seal, Fine & Gross Leak	1071	100%	✓	
	Final Electricals	3411,3413,3421,3404	100%	✓	
Group A Inspection (Conformance)	A-2 DC Static Tests at 25°C	3411,3413,3421,3404	116	✓	
	A-3 High & Low Temp DC Static Tests	3411,3413,3421,3404	116	✓	
	A-7 Gate Charges	3471 Condition B	45	✓	
	A-7 Capacitance	3473	45	✓	
Group B Inspection (Conformance)	B-1, B-2, B-3, B-4, B-5	Sample base equivalent to a MIL-PRF-19500 flow or as required by procurement specificcation			
Group C Inspection (Conformance)	C-1, C-2, C-3, C-4, C-6, C-7	Sample base performed yearly per package style equivalent to a MIL-PRF-19500 flow or as required by procurement specification			
Group D Inspection (Conformance)	TID	1019	15	✓	
	SEE	1080	5	✓	
Group E Inspection (Qualification Inspection)	E-1, E-2, E-5, E-6 E-7	Performed during product introduction or a major process change equivalent to a MIL-PRF-19500 flow or as required by procurement specification			
	E8 Switching				

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Patents

EPC Corporation and EPC Space hold numerous worldwide patents. Any that apply to the product(s) listed in this document are identified by markings on the product(s) or on internal components of the product(s) in accordance with local patent laws.

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Revisions

Datasheet Revision	Product Status
REV P#	Proposal/development
REV Q#	Characterization and Qualification
M-700-007-F	Production Released

Information subject to change without notice.
Revised February, 2023