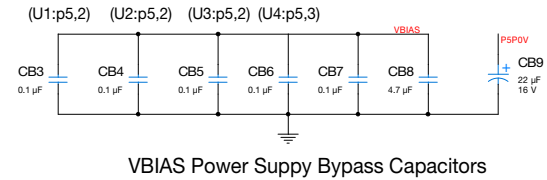


NOTES:

- 1.) All capacitors are X7R and 50V, unless otherwise specified.
- 2.) Place bypass capacitors directly beneath associated IC's.
- 3.) End-user may omit resistors R2 and R3 and populate potentiometers RA1 and RA2 (Vishay TS63Y type) to adjust dead time.
- 4.) All resistors are 0805-size unless otherwise specified.
- 5.) Resistors RB1 and RB2 select if direct VBIAS or clamped VBIAS protection is provided for the DUT. RB1 provides direct, and is populated. RB2 provides the clamped protection, and is NOPOP.
- 6.) Inductor L1 is a Vishay IHLP 6767 size, and is populated. Inductor L2 is a Bourns PQ2614BLA size, and is NOPOP.
- 7.) Connector JTST is provided to accurately measure conversion efficiency at various operating conditions.

Figure 11. EPC7C005/FBS-GAM02 POL Evaluation Board Schematic Diagram

Optional Output Filter Capacitors (Tantalum D-Case, EIA 7434)



VBIAS Power Supply Bypass Capacitors