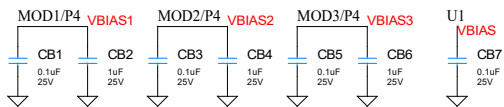
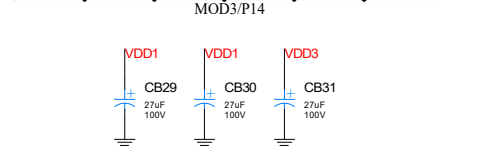
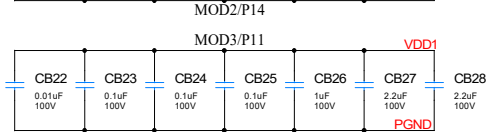
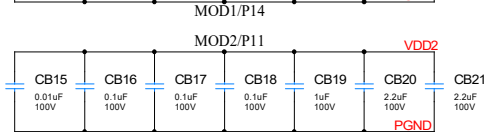
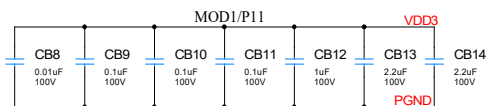


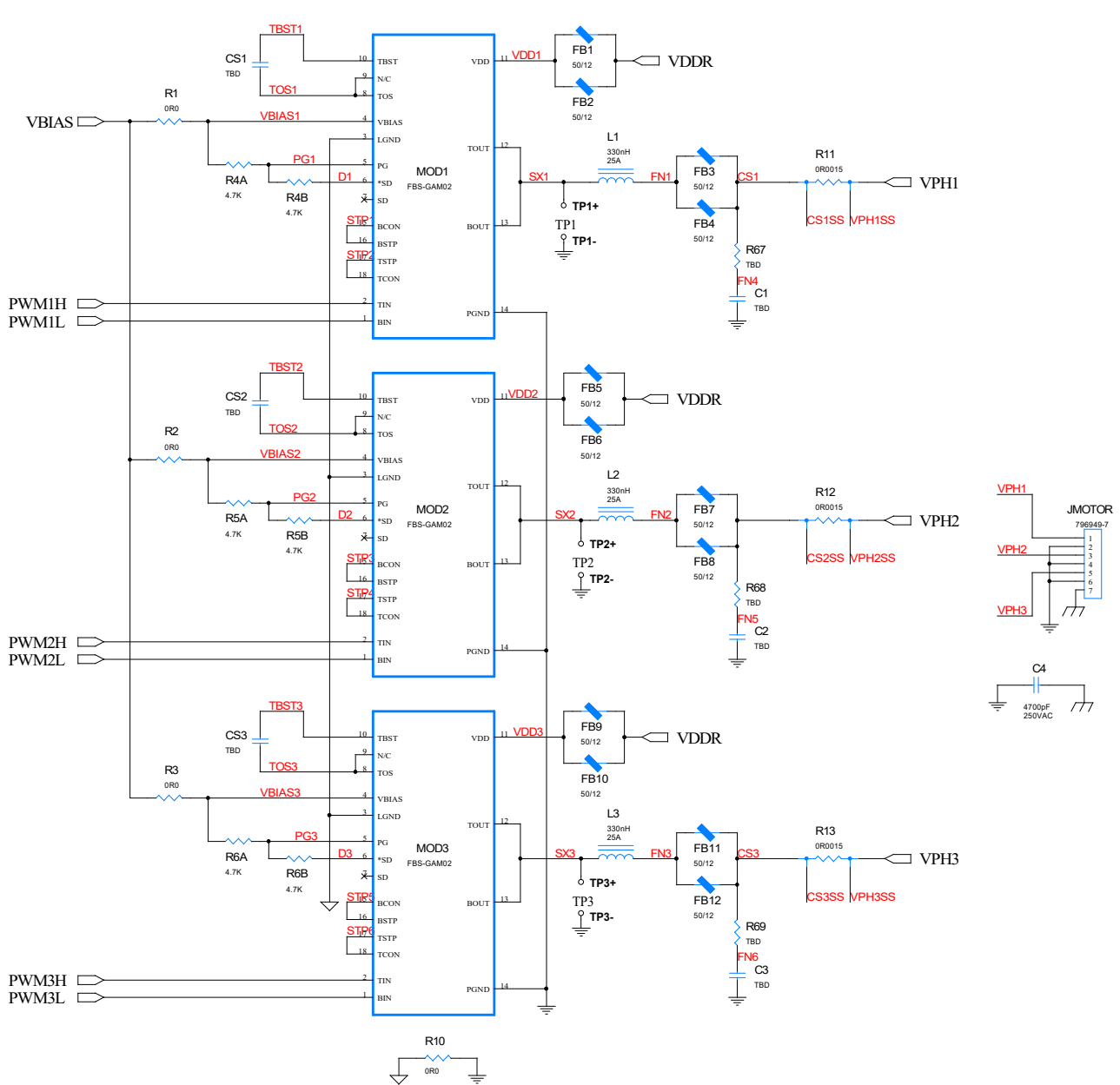
Power Good Indicators



Logic Bypass Capacitors



VDD Bypass Capacitors



Three-Phase Half-Bridge Power Driver Stage

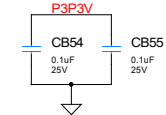
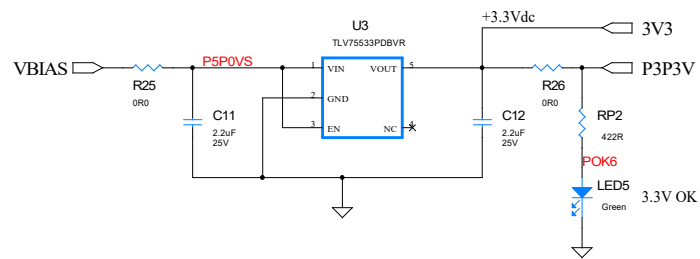
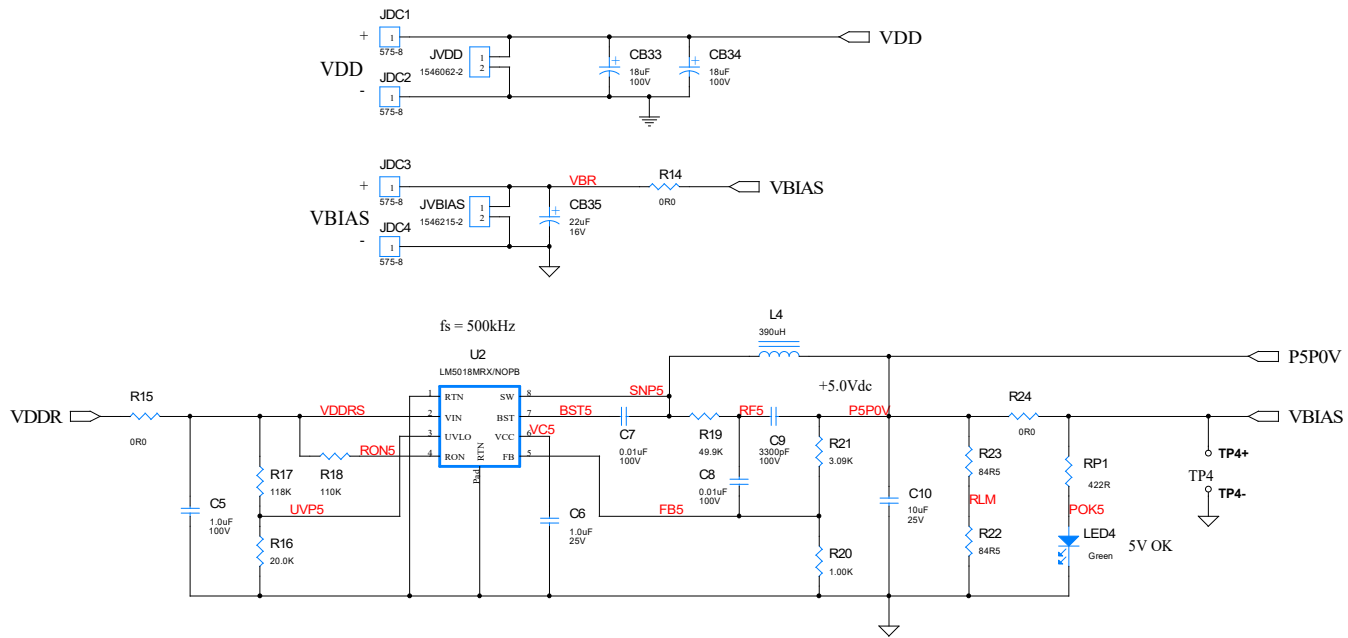
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File: **FBS-GAM02 Motor Driver Demo Board**

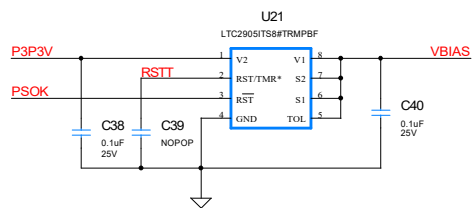
Size	Document Number	Rev
C	Analog Motherboard	E4

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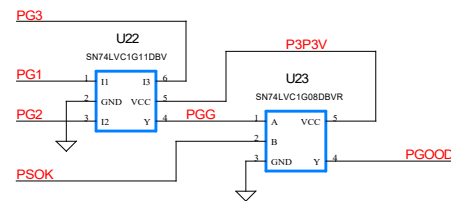


Motherboard Power Supplies

Logic Bypass Capacitors



Power Supply Monitor

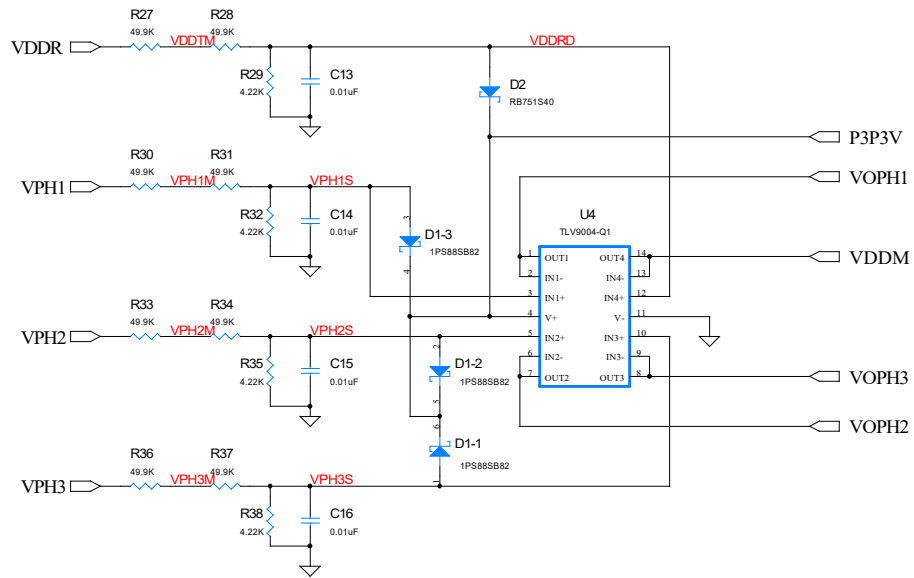


Power Good Logic

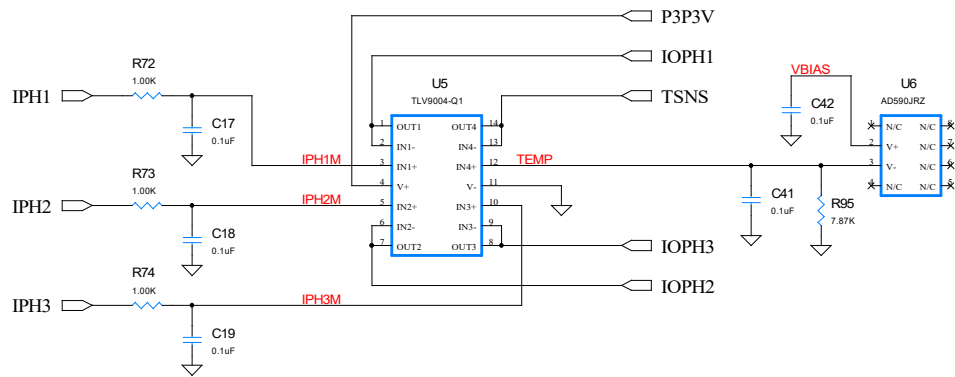
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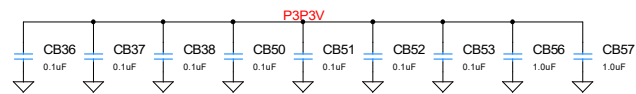
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Size: C	Document Number: Analog Motherboard	Rev: E4
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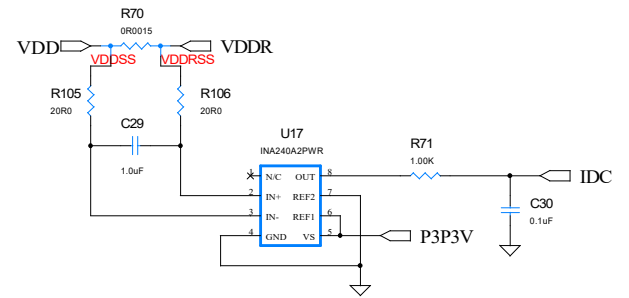
VDD and Phase Voltage Signal Conditioning (VDD, VPH * 0.05)



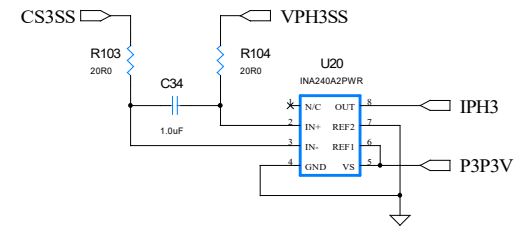
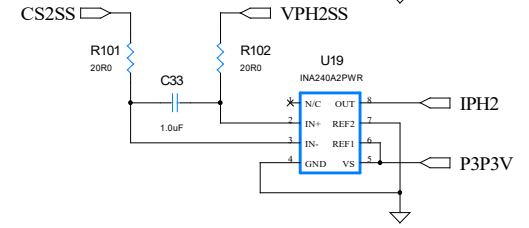
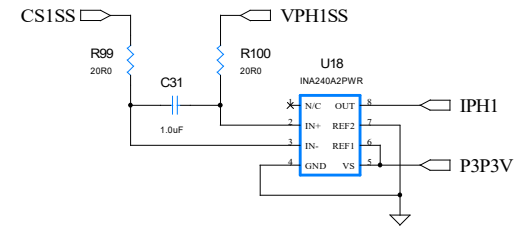
Temperature and Phase Current Signal Conditioning (IPH * 0.1)



Bypass Capacitors



VDD DC Current Sense Amplifier (IDC * 0.1)



Phase Current Sense Amplifiers (IP * 0.1)

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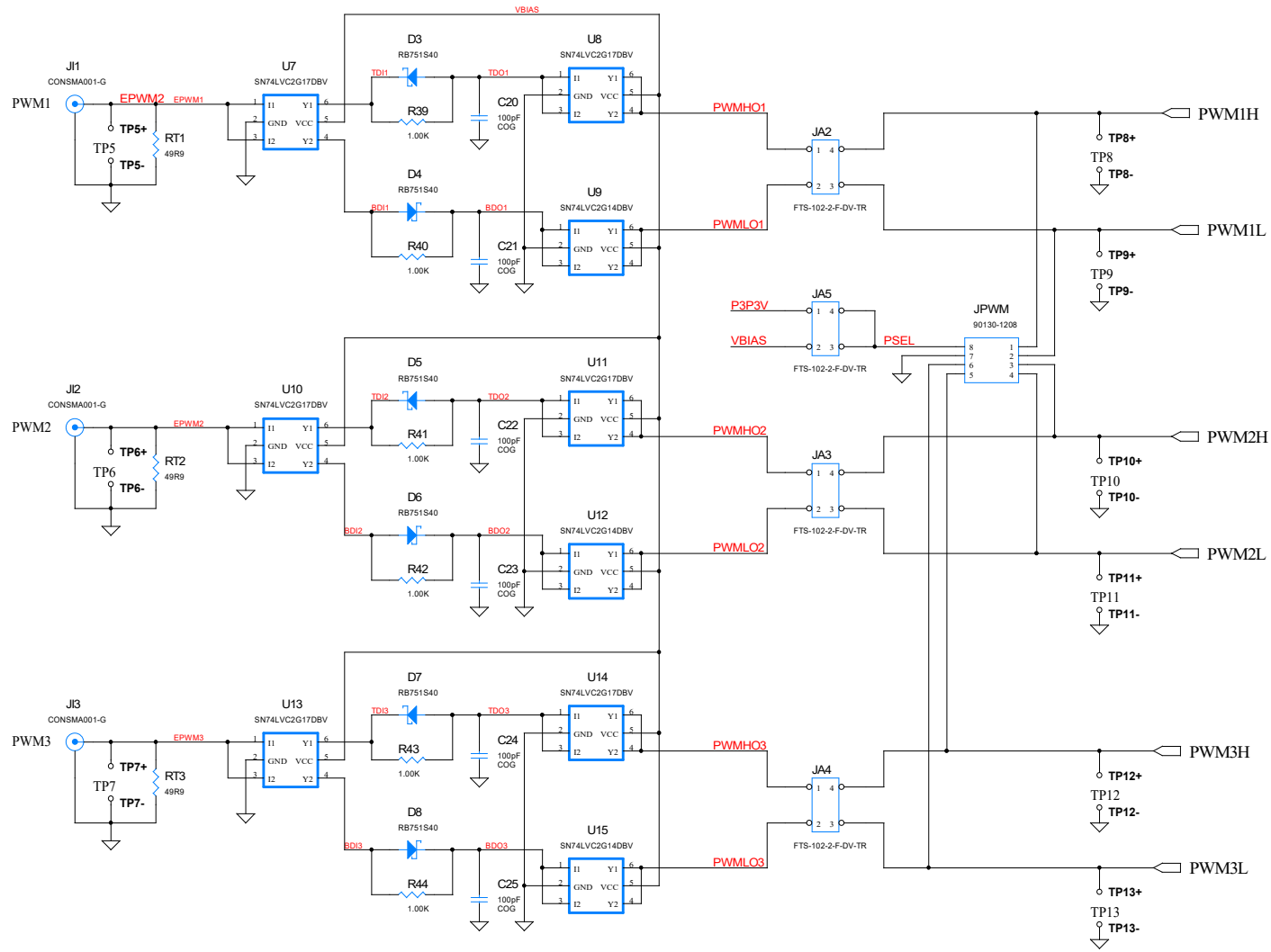
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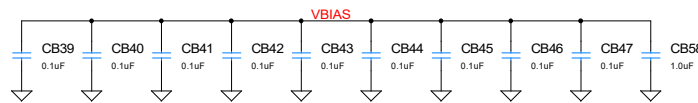
Part # **FBS-GAM02 Motor Driver Demo Board**

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Three-Phase Dead Time Generation



Logic Bypass Capacitors

2.) For 3.3V power to JPWM place a jumper M50-2000005 across pins 1 and 4 of JA5. For 5.0V power to JPWM place a jumper M50-2000005 across pins 2 and 3 of JA5.

1.) To enable PWM inputs J11, J12 and J13, place shunt jumpers M50-2000005 across both sets of pins on JA2, JA3 and JA4, and omit six sets of jumpers on JA1.

NOTES:

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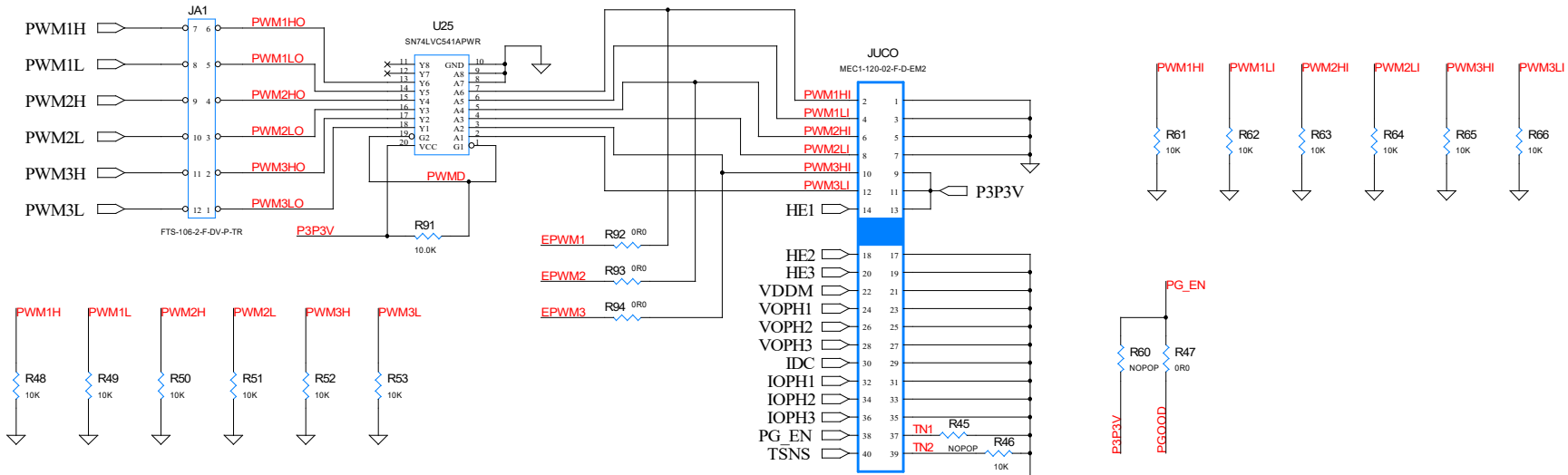
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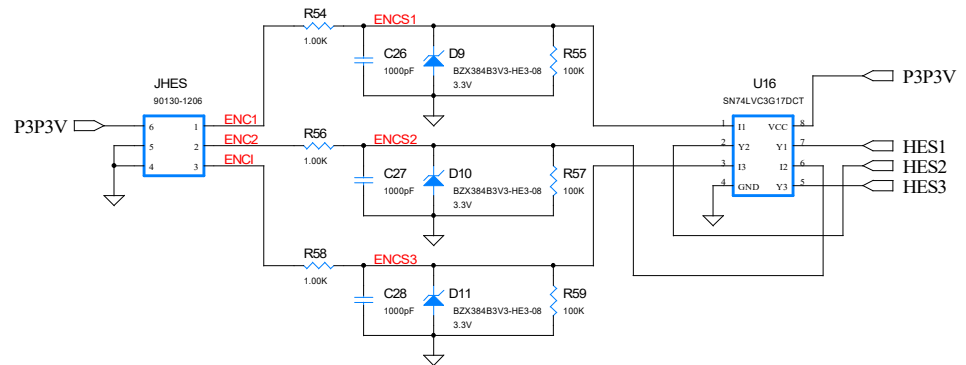
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Rev		
FBS-GAM02 Motor Driver Demo Board		
Size	Document Number	Rev
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Date:	Saturday, July 16, 2022	Sheet 4 of 5

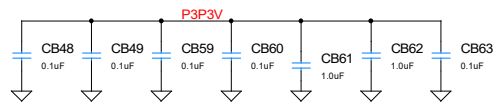
PWM Disconnect



EPC Microcontroller Interface

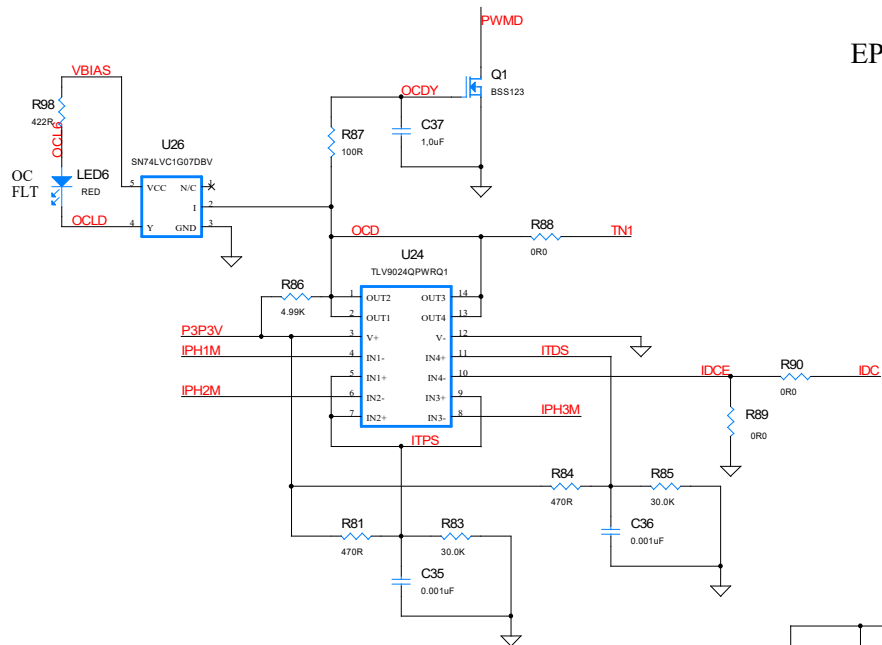


Hall Effect/Priority Encoder Signal Conditioning



Logic Bypass Capacitors

Over-Current Detection



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1.) To enable PWM inputs from JUC0, place shunt jumpers M50-2000005 across the six sets of pins on AJ1 and omit the three pairs of jumpers on JA2, JA3 and JA4.

NOTES: