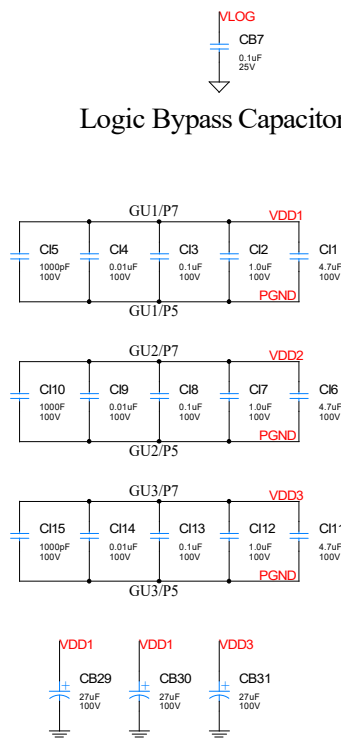
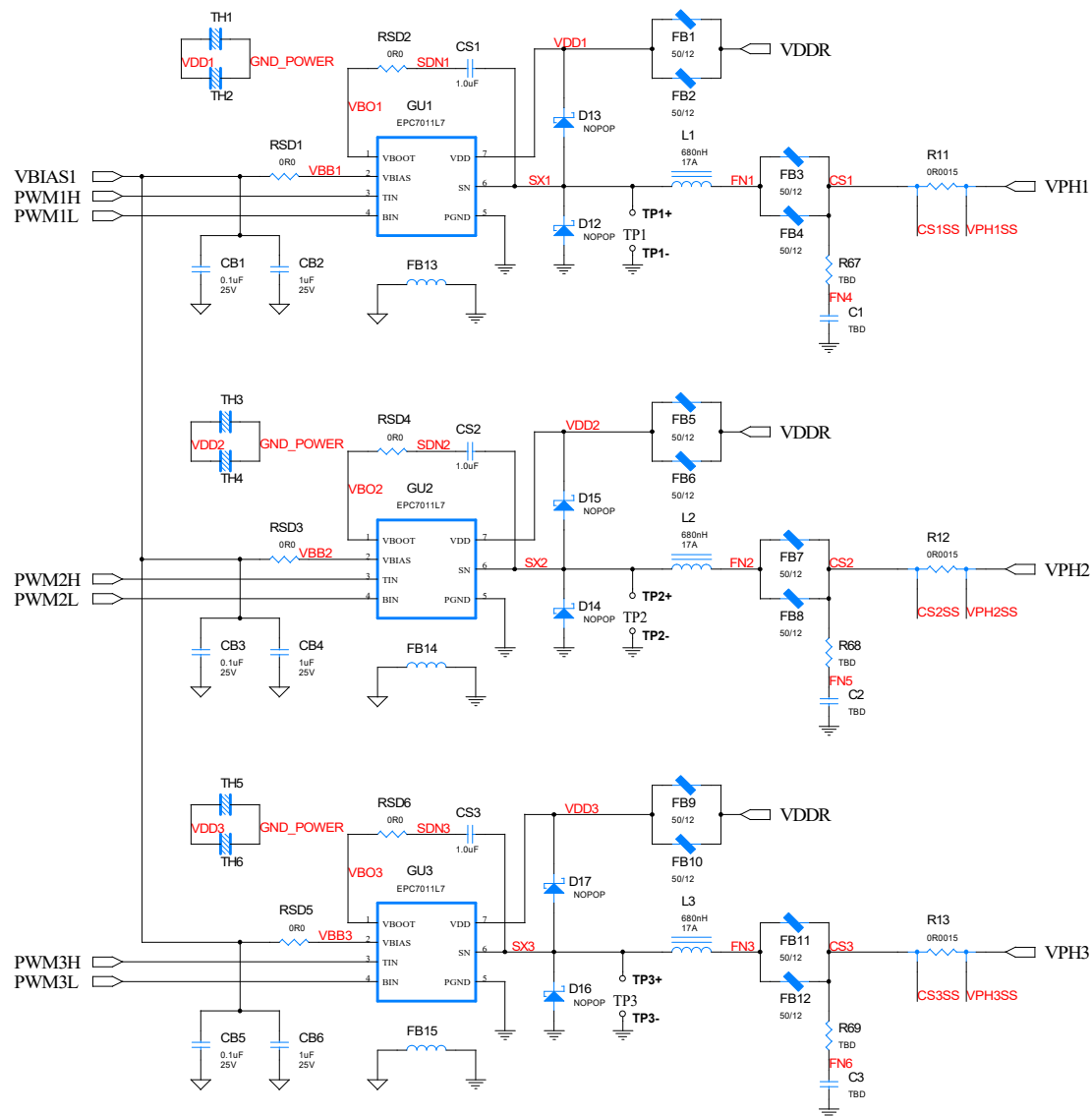


Logic Bypass Capacitors



VDD Bypass Capacitors



Three-Phase Half-Bridge Power Driver Stage

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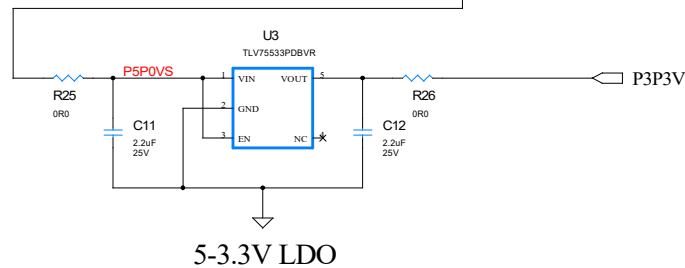
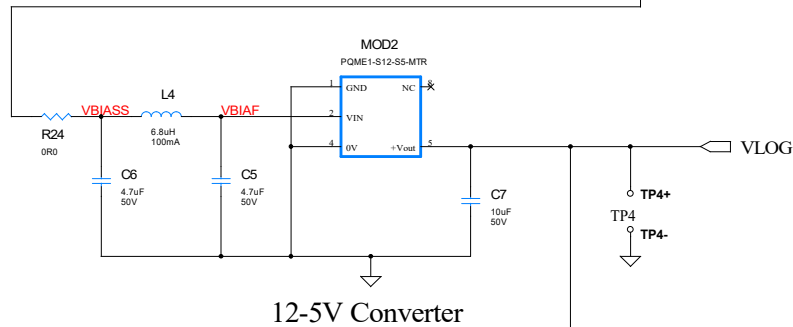
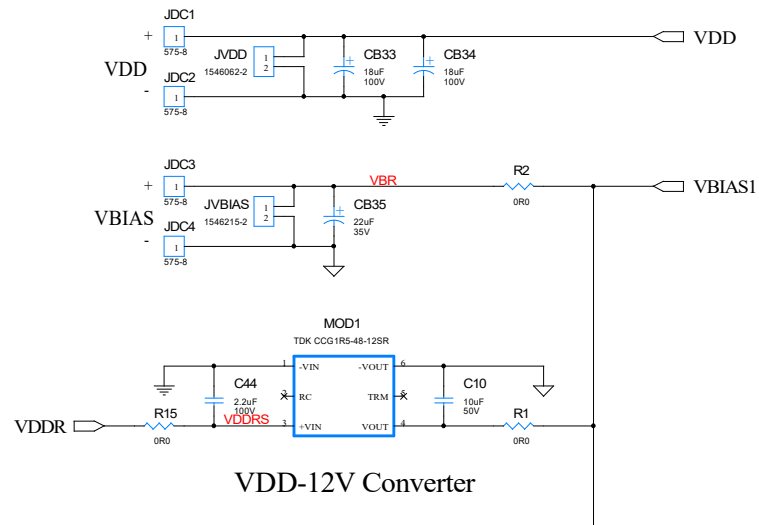
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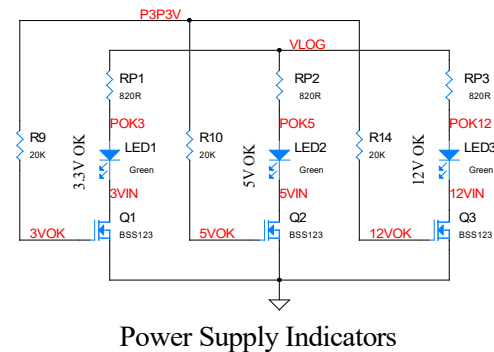
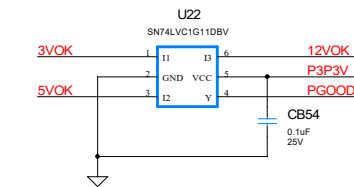
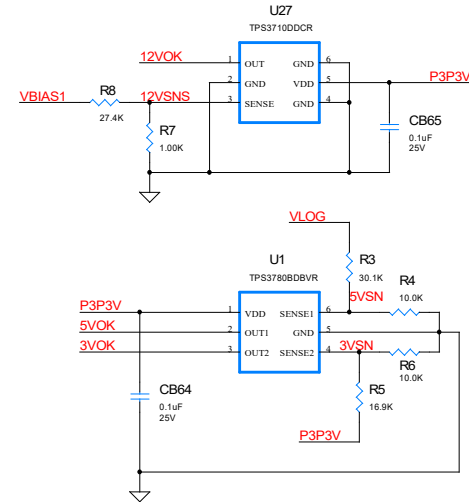
EPC7011L7 Motor Driver Demo Board

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Motherboard Power Supplies



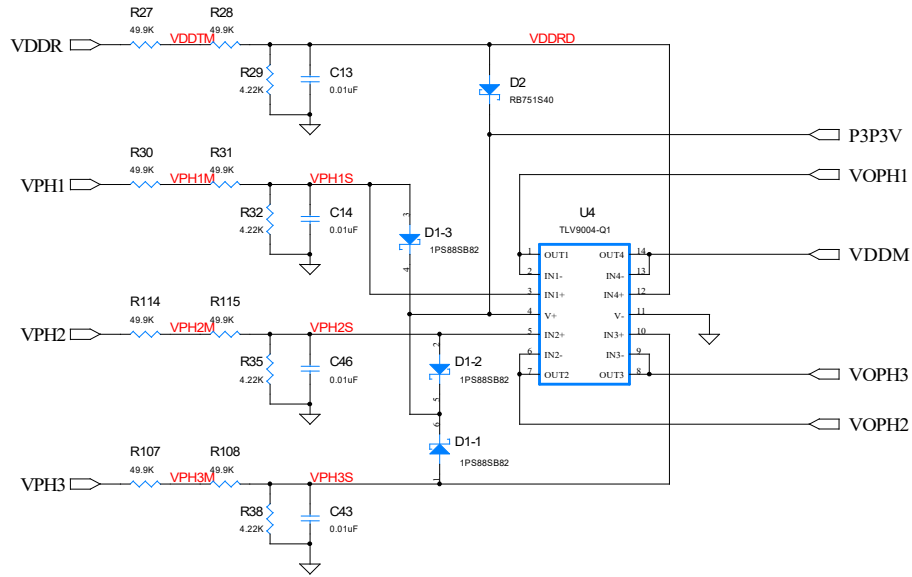
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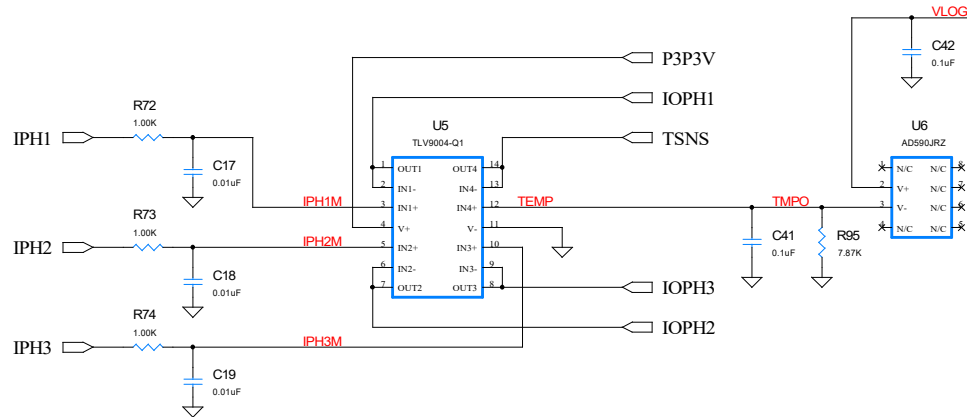
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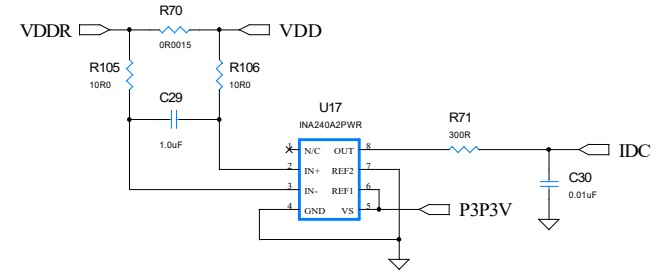
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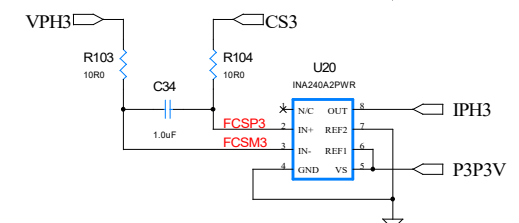
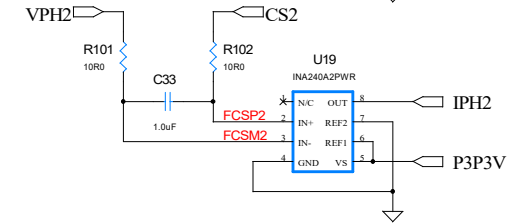
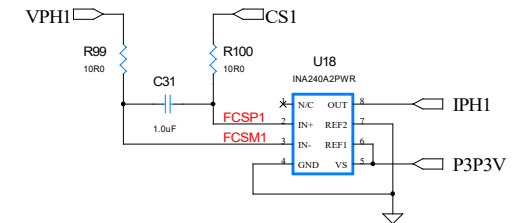
VDD and Phase Voltage Signal Conditioning (VDD, VPH * 0.05)



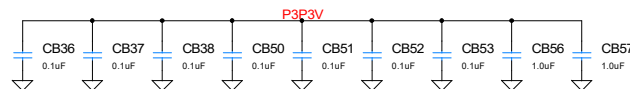
Temperature and Phase Current Signal Conditioning (IPH * 0.1)



VDD DC Current Sense Amplifier (IDC * 0.1)



Phase Current Sense Amplifiers (IP * 0.1)



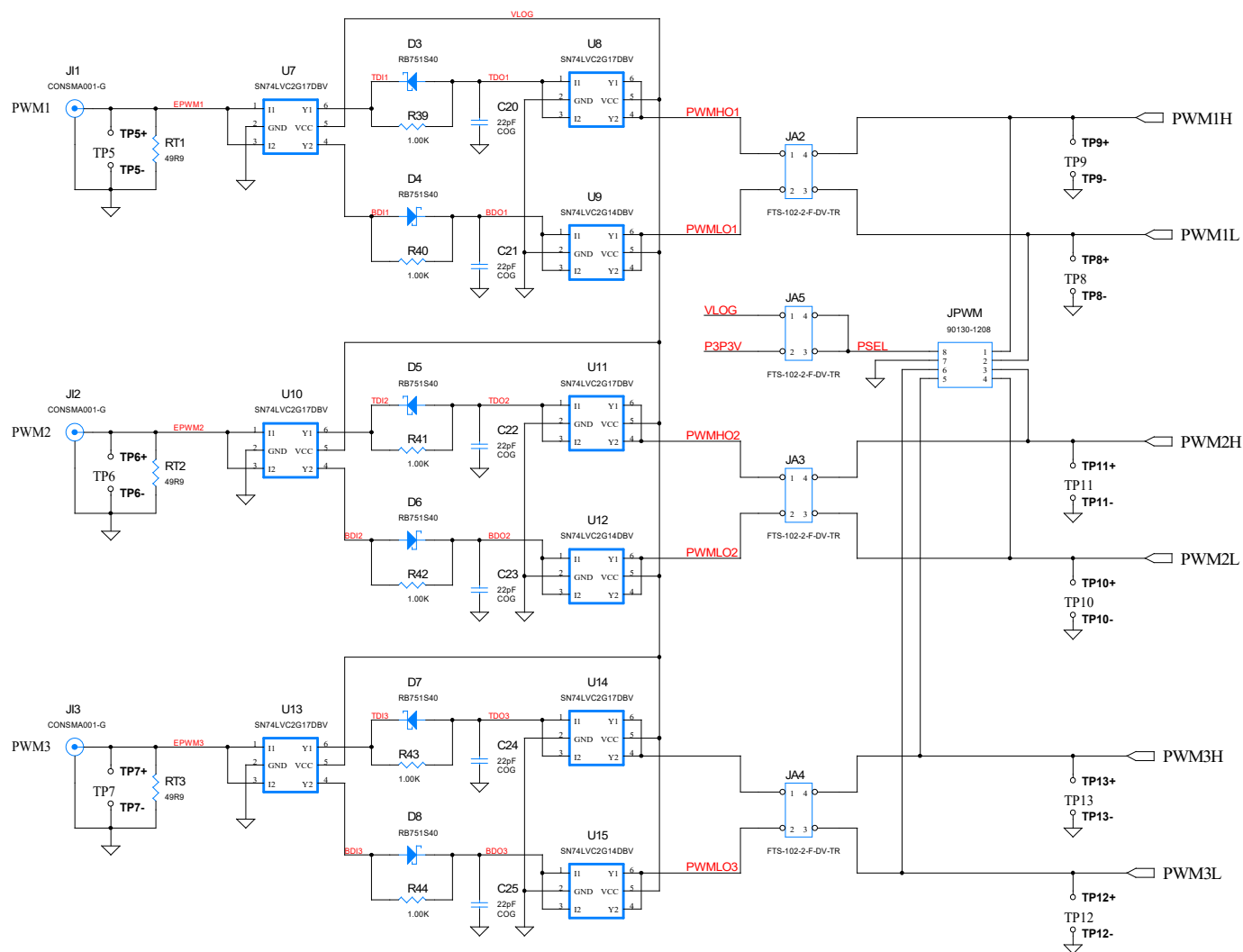
Bypass Capacitors

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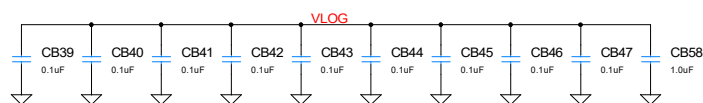
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Three-Phase Dead Time Generation



Logic Bypass Capacitors

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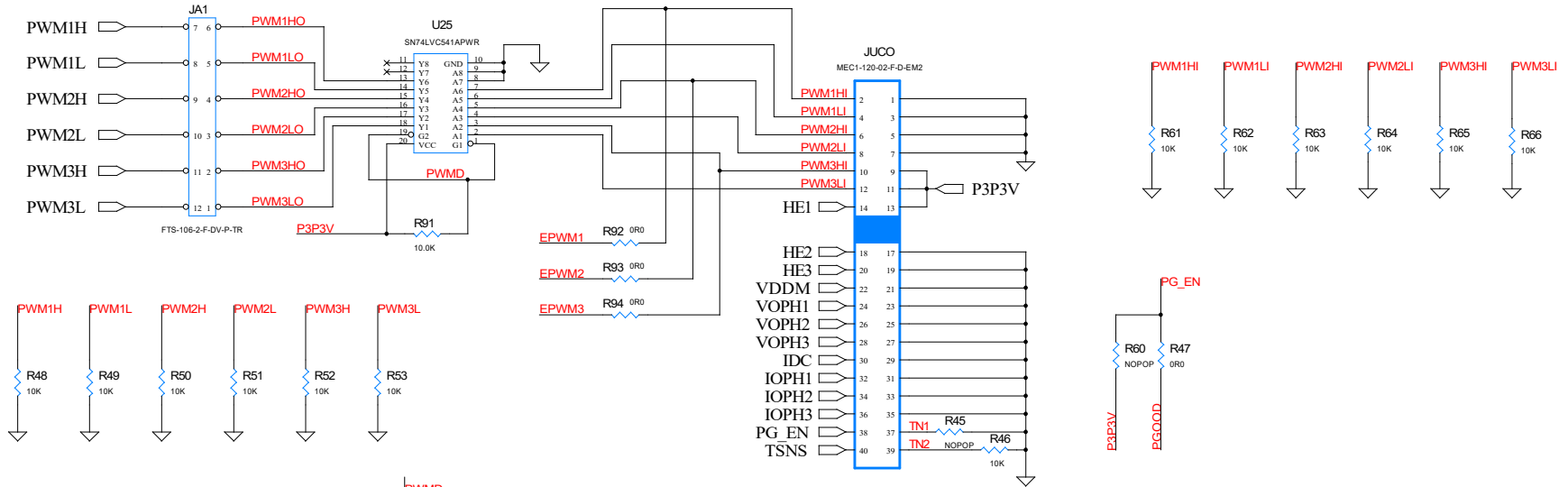
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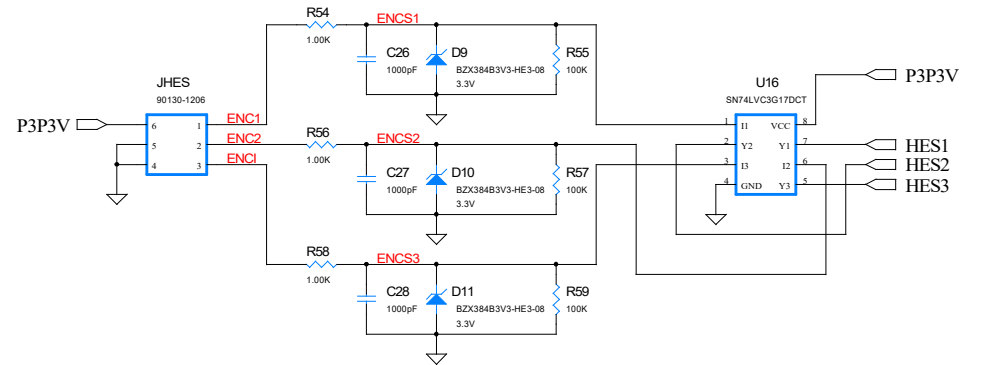
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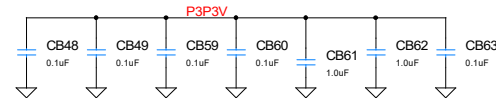
PWM Disconnect



EPC Microcontroller Interface

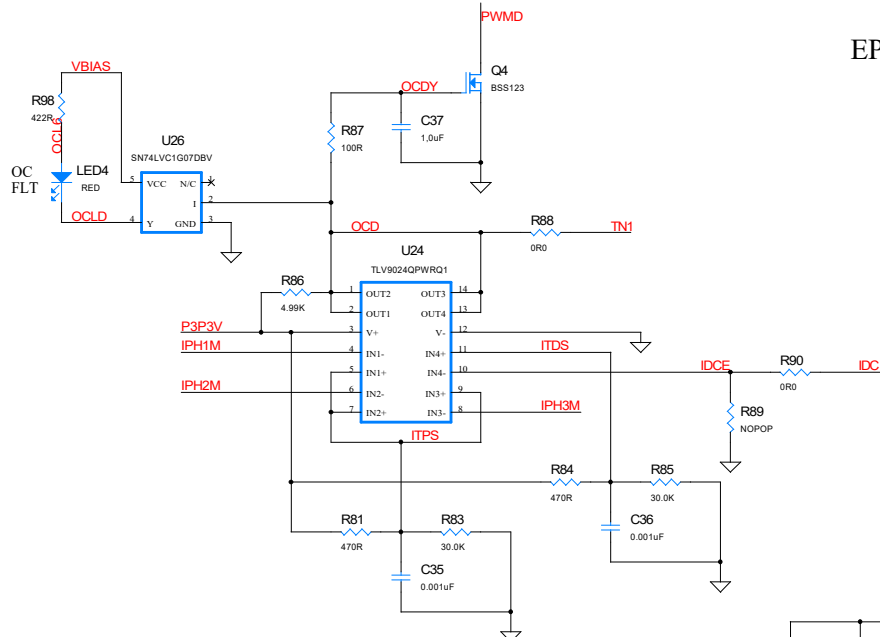


Hall Effect/Priority Encoder Signal Conditioning



Logic Bypass Capacitors

Over-Current Detection



1.) To enable PWM inputs from JUCO, place shunt jumpers M50-2000005 across the six sets of pins on JA1 and omit the three pairs of jumpers on JA2, JA3 and JA4.

NOTES:

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